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 SIMULTANEOUS BIDIRECTIONAL INPUT/OUTPUT CIRCUIT AND METHOD
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FIG. 1 (PRIOR ART)

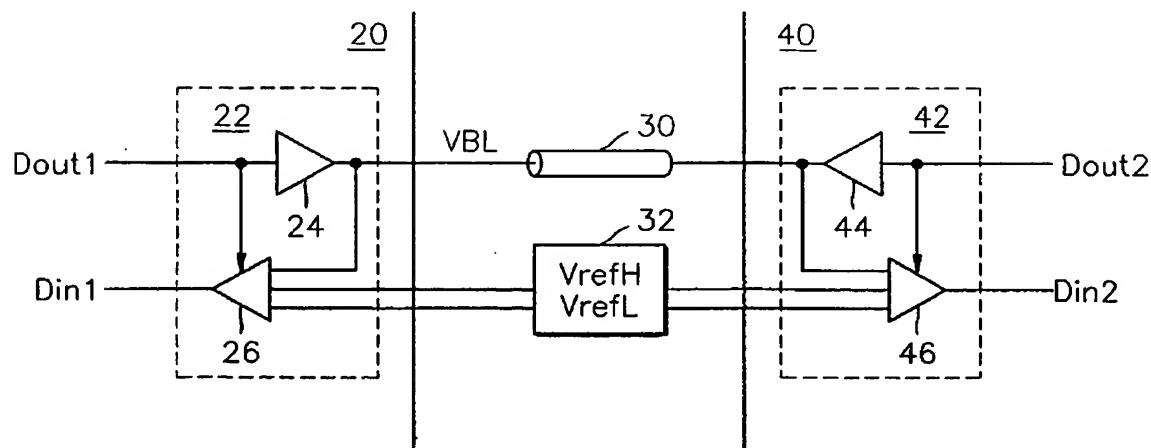
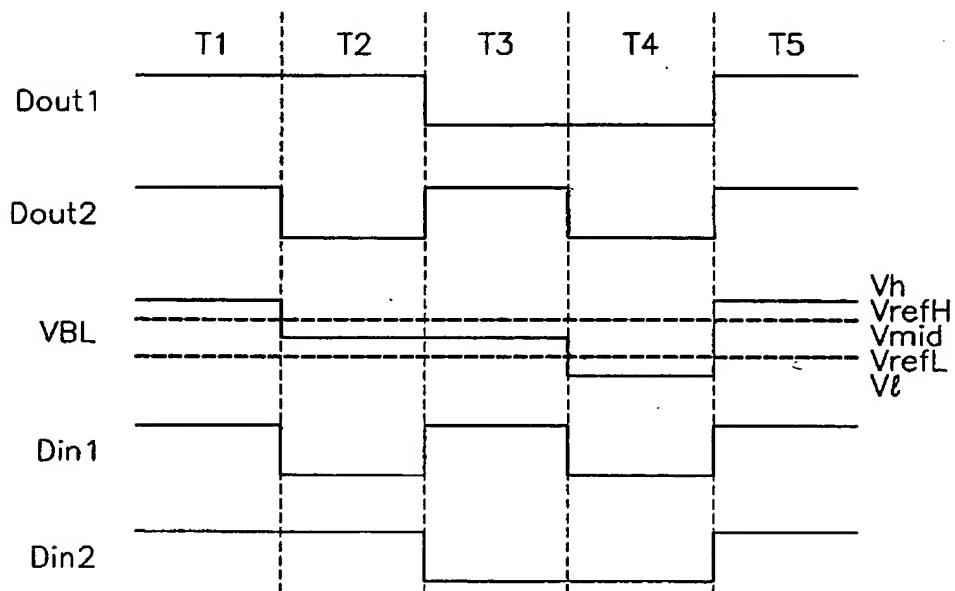


FIG. 2 (PRIOR ART)



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FIG. 3A (PRIOR ART)

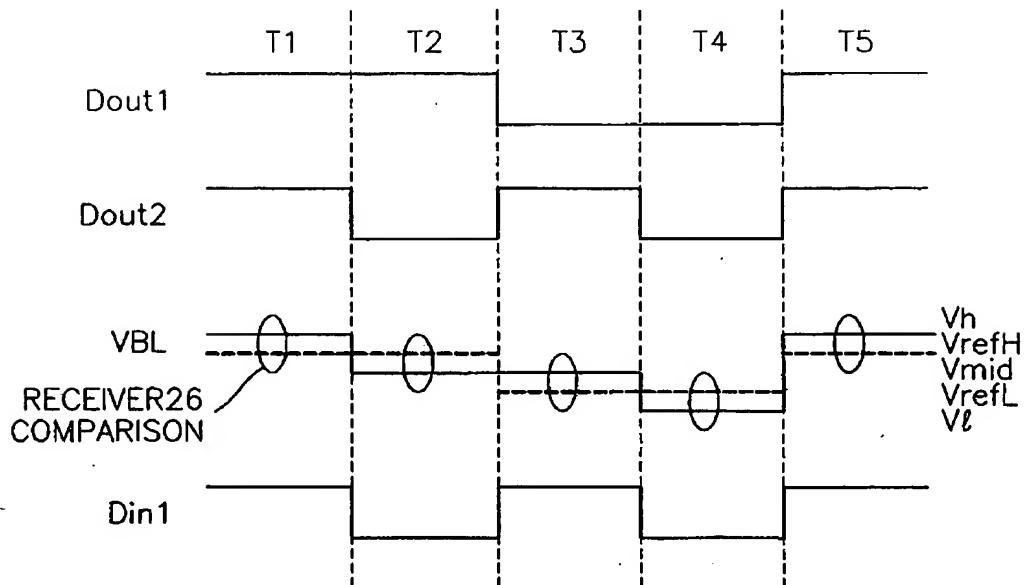
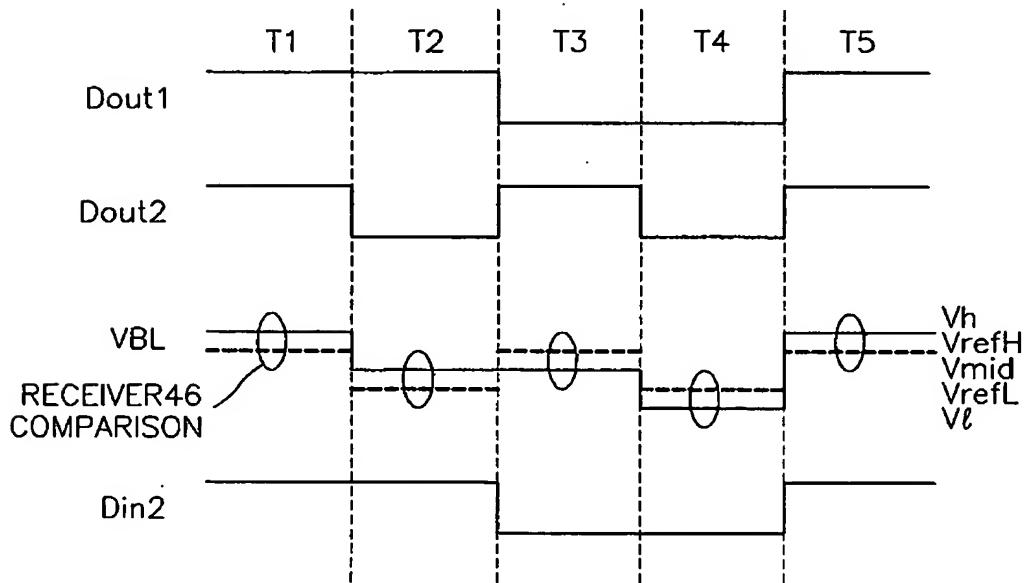


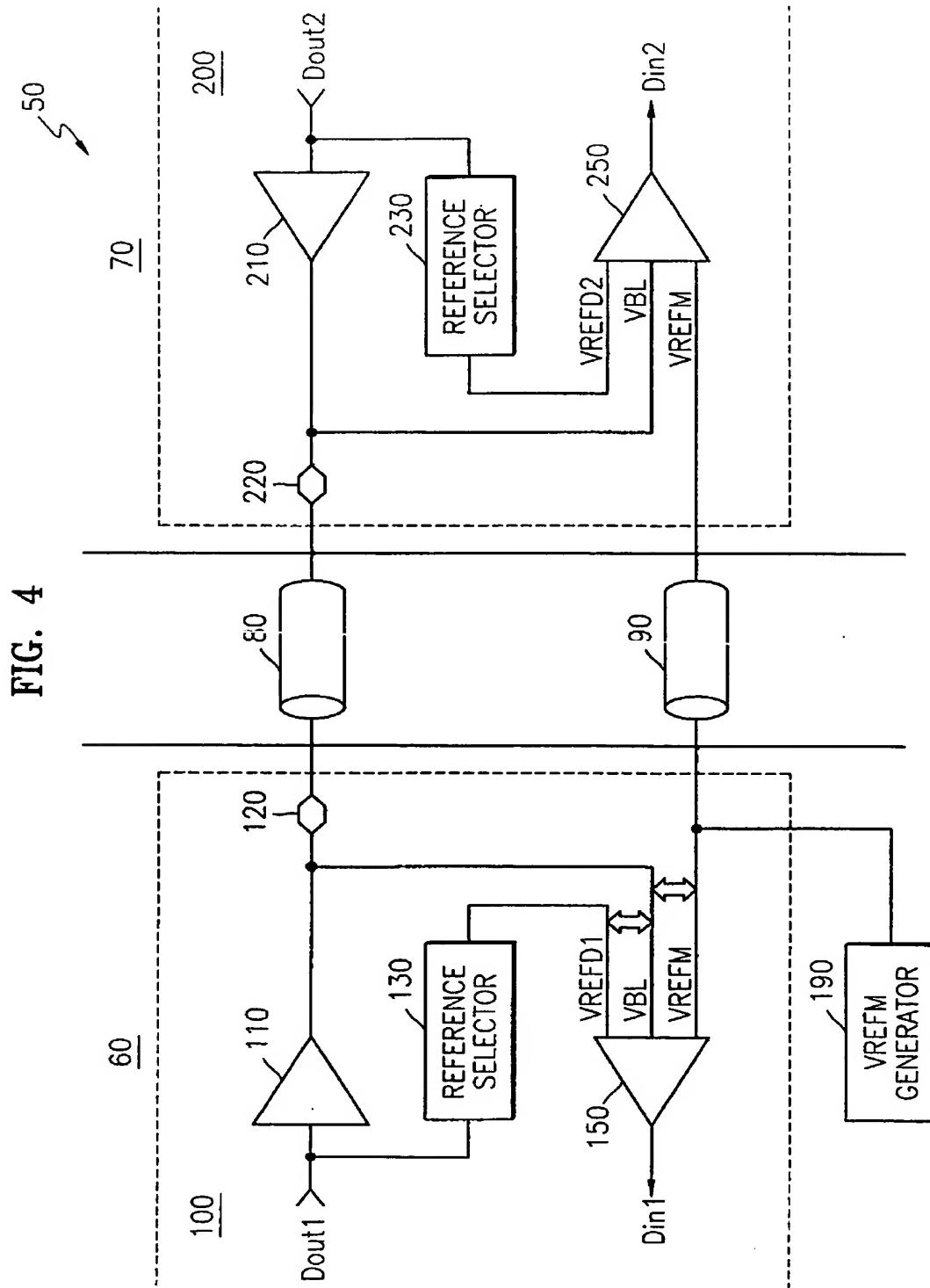
FIG. 3B (PRIOR ART)



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FIG. 5A

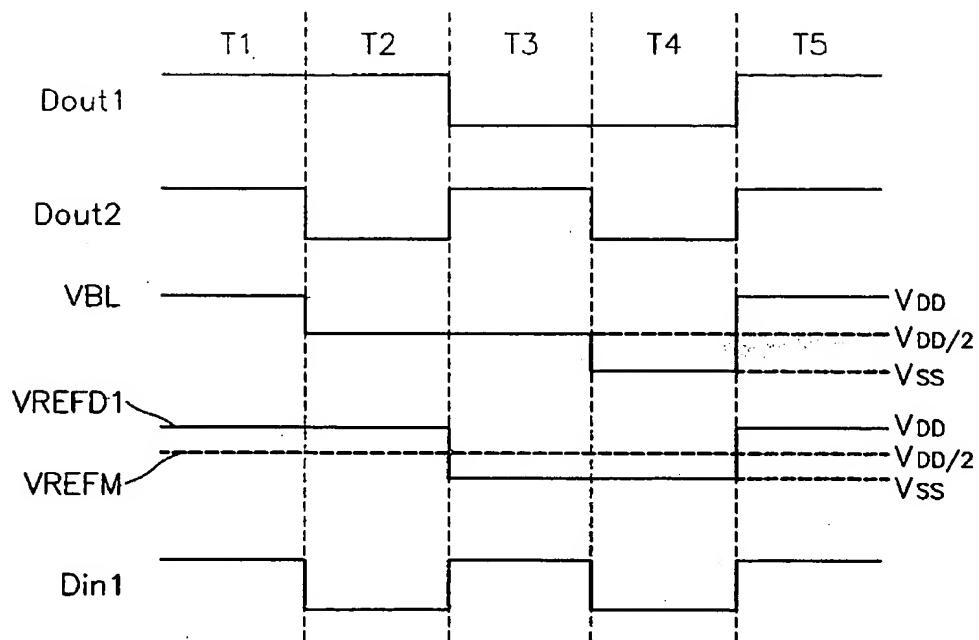
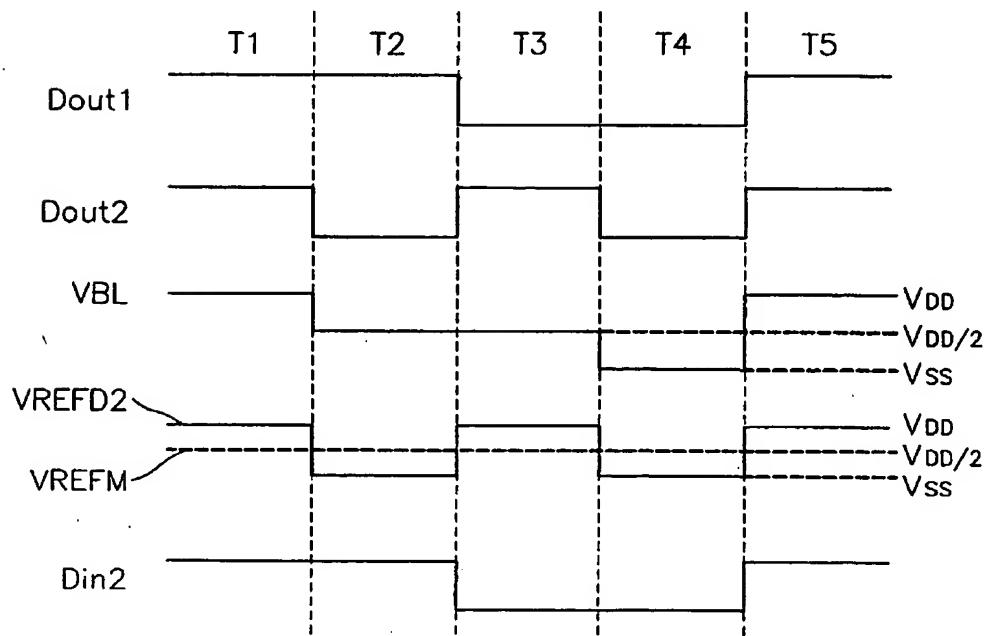


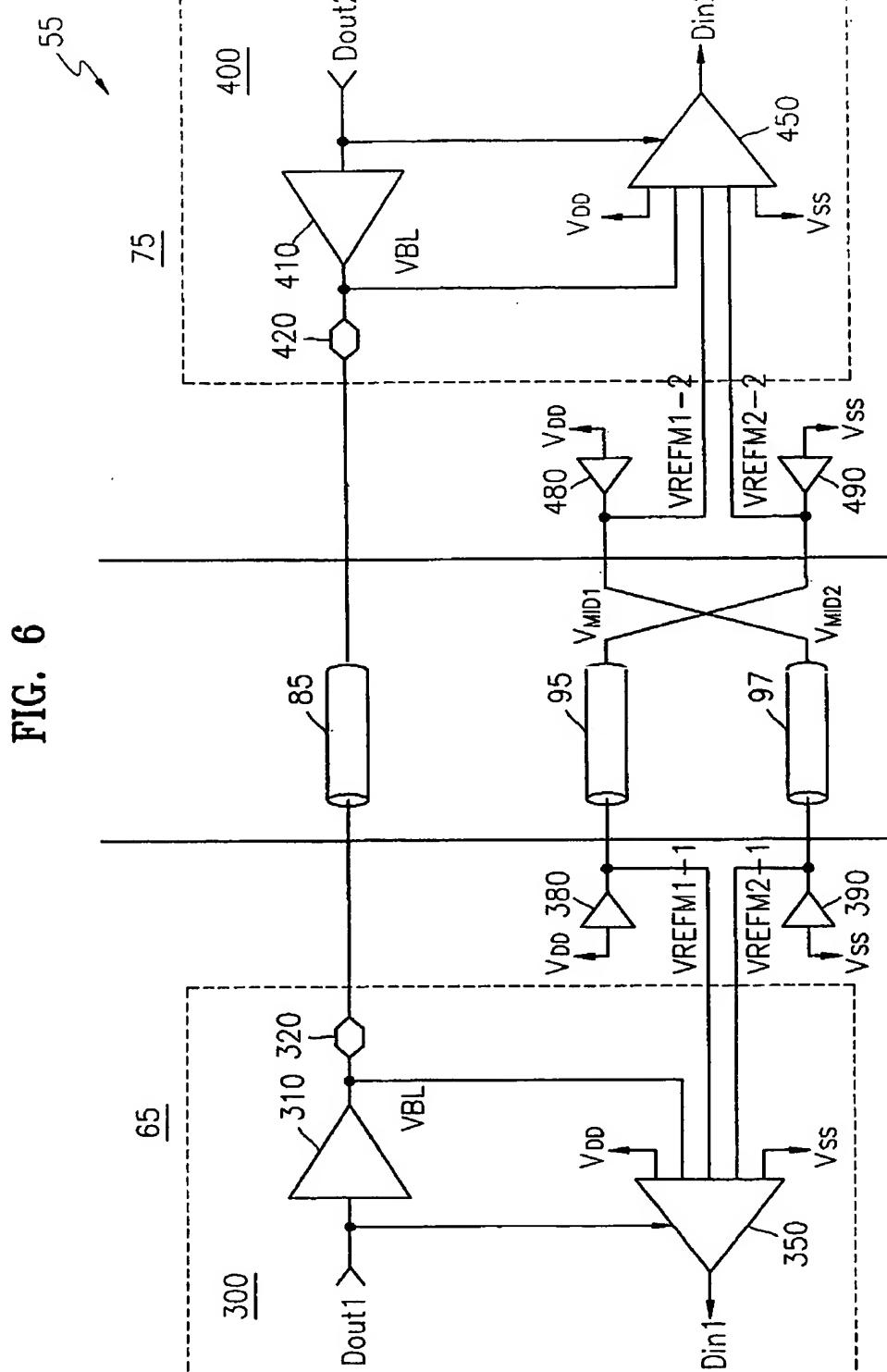
FIG. 5B



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FIG. 7A

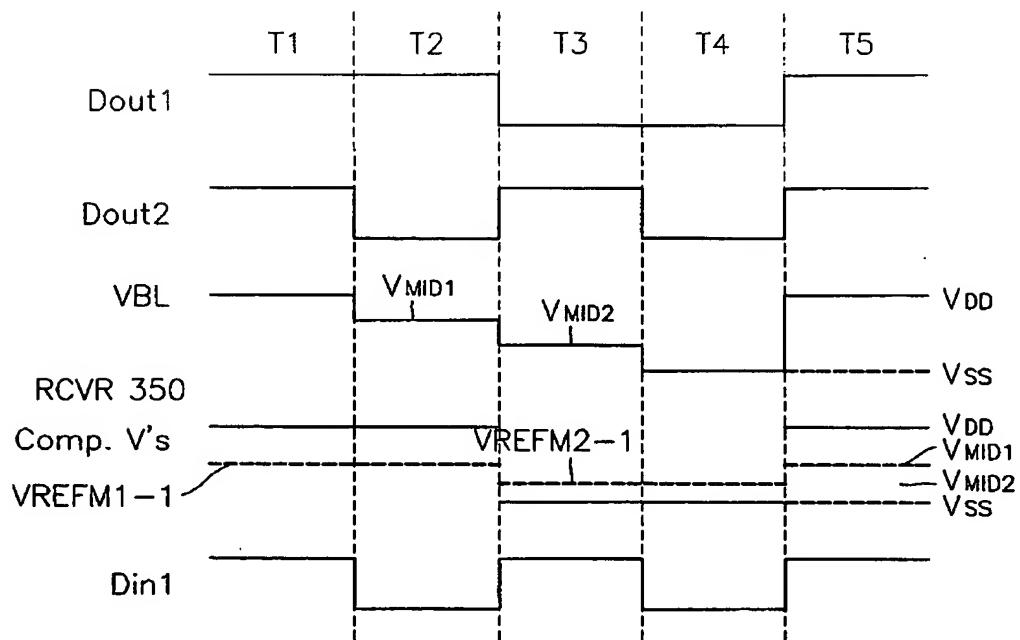
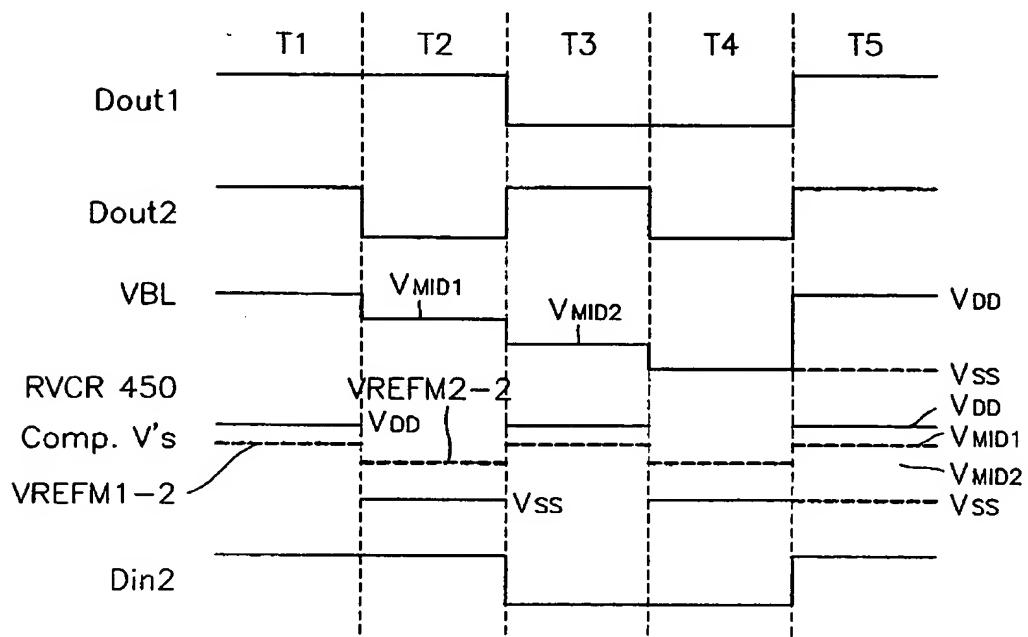


FIG. 7B

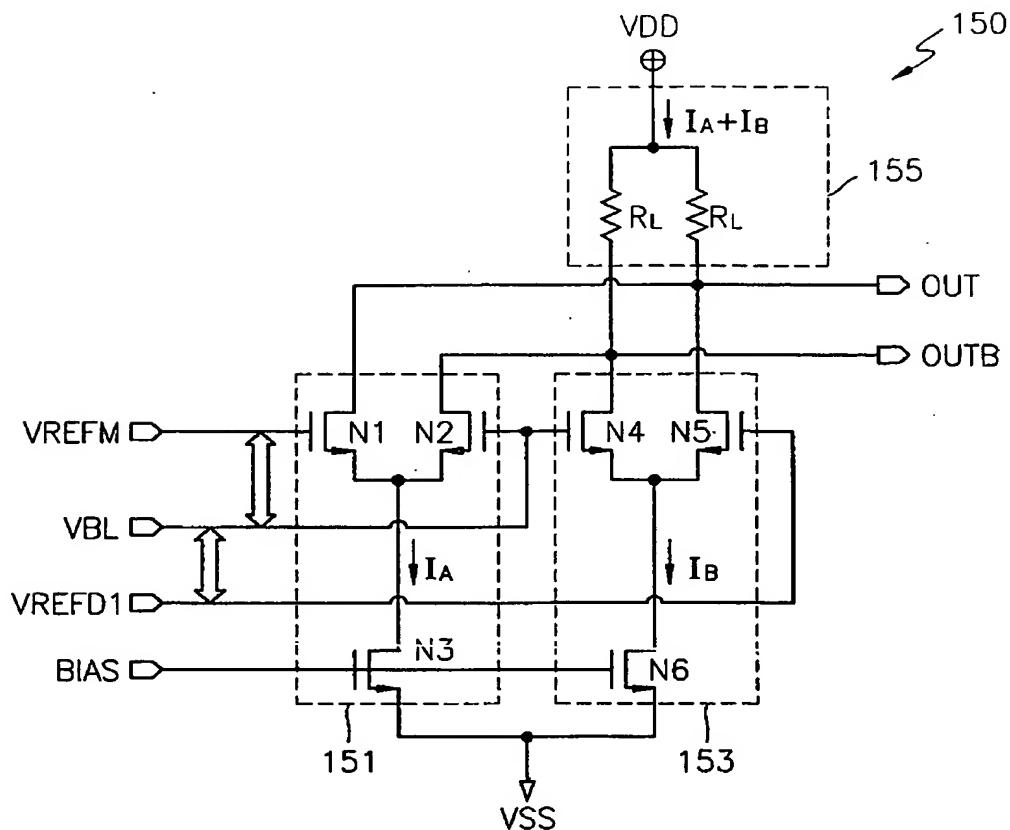


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FIG. 8



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FIG. 9

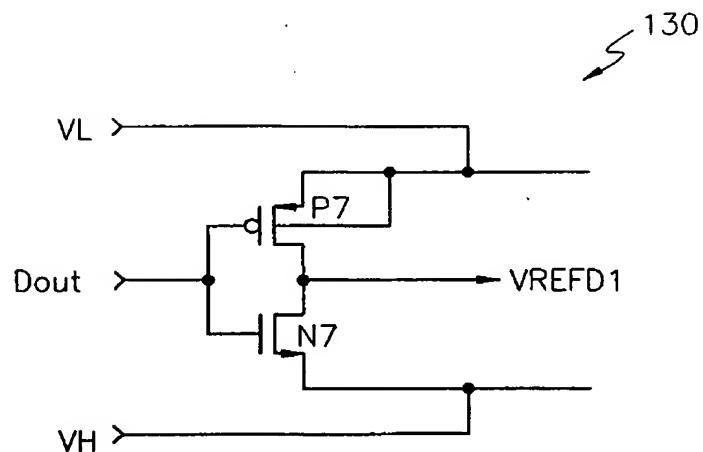
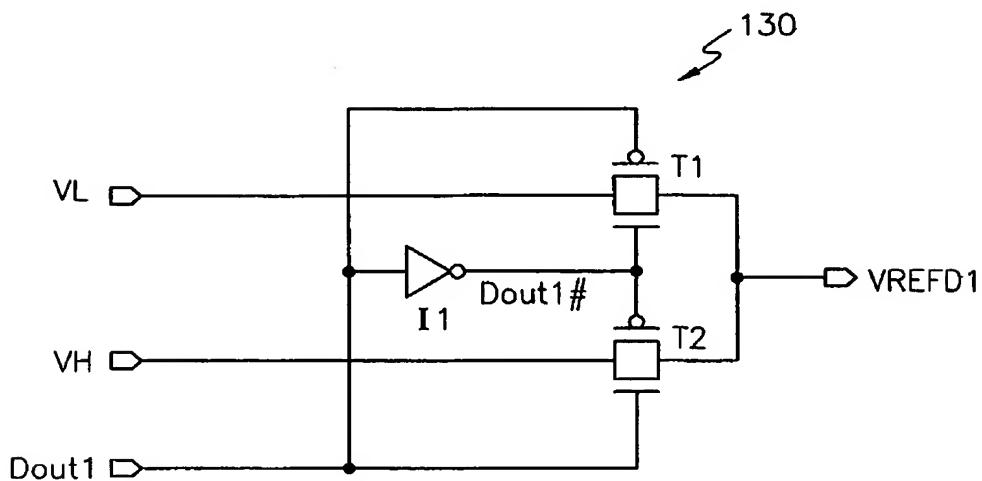


FIG. 10

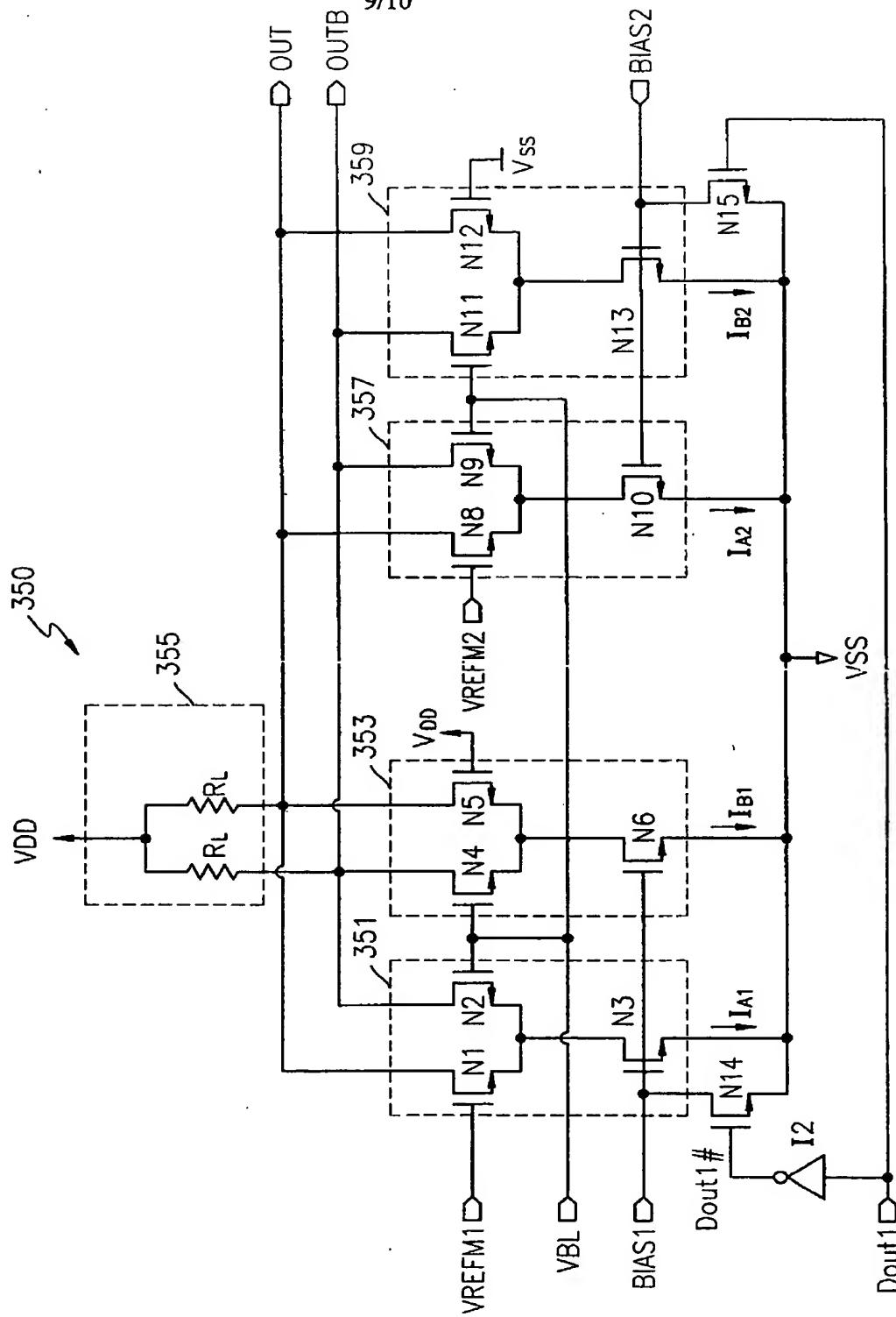


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FIG. 11



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FIG. 12

